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		Subclass	ISSUE CLASSIFICATION
	Class		

PATENT NUMBER

U.S. UTILITY Patent Application

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SCANNED TO 4 Q.A. *Am*

APPLICATION NO.	CONT/PRIOR	CLASS 365 711	SUBCLASS 154	ART UNIT 2706 2818 2702	EXAMINER K. Seng J. Seng
APPLICANTS	Sanjay Talreja Jason Panavich Ramkarthik Ganesan Terry Kendall				
TITLE	'Status register architecture for flexible read-write device				

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs: Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)	(Date)			
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	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)	(Date)			

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